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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/787,353	02/26/2004	Peter Ryser	X-1503 US	4877
24309 7590 01/29/2007 XILINX, INC ATTN: LEGAL DEPARTMENT 2100 LOGIC DR SAN JOSE, CA 95124			EXAMINER PATEL, ANAND B	
			ART UNIT 2116	PAPER NUMBER
SHORTENED STATUTORY PERIOD OF RESPONSE		MAIL DATE	DELIVERY MODE	
3 MONTHS		01/29/2007	PAPER	

Please find below and/or attached an Office communication concerning this application or proceeding.

If NO period for reply is specified above, the maximum statutory period will apply and will expire 6 MONTHS from the mailing date of this communication.

Office Action Summary	Application No.	Applicant(s)	
	10/787,353	RYSER, PETER	
	Examiner	Art Unit	
	Anand Patel	2116	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 12 December 2006.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 13, 15, 16 and 18-34 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☒ Claim(s) 19-24 is/are allowed.
- 6) ☒ Claim(s) 13, 15, 16 and 18 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | Paper No(s)/Mail Date. _____ |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08) | 5) <input type="checkbox"/> Notice of Informal Patent Application |
| Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

1. Amendment filed 12/12/06 has been entered and as such claims 13, 15-16, 19 are amended and claim 14 is canceled.

Claim Rejections - 35 USC § 103

2. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

3. Claims 13, 15-16, 18 are rejected under 35 U.S.C. 103(a) as being unpatentable over US Patent No 6711059 to Sinclair et al (Sinclair) in view of US Patent No 6711675 to Spiegel et al (Spiegel).

- As per claim 13, Sinclair discloses a method of controlling a processor within a data processing system, comprising:
 - Maintaining said processor in a halt condition in response to reset information from said data processing system (column 5, lines 18-20, 31-33);
 - Configuring at least one memory resource in communication with said processor (column 4, lines 66-67; column 5, lines 27-30); and
 - Releasing said processor from said halt condition (column 5, lines 52-56).

Sinclair also discloses wherein said configuring step comprises storing data in said at least one memory resource (column 4, lines 66-67; column 5, lines 27-30). Sinclair fails to explicitly disclose wherein the at least one memory resource is at a location associated with a reset vector of said processor. Spiegel teaches at least one memory resource is at a location associated with a reset vector of said processor (column 2, lines 21-24; figure 1). An advantage of the system taught by Spiegel is the ability to verify a boot sequence in order to anticipate and investigate

potential problems (column 1, lines 56-60). It would have been obvious to one of ordinary skill in the art at the time of invention to modify Sinclair with the reset vector pointing to firmware as taught by Spiegel. Motivation to modify is to improve system reliability and decrease boot loading issues.

- As per claim 15, Sinclair discloses the method wherein said data comprises code configured to be executed by said processor (column 5, lines 41-48).
- As per claim 16, Sinclair discloses the method wherein said processor is released from said halt condition in response to storage of said data (column 5, lines 41-48, 52-56).
- As per claim 18, Sinclair discloses the method wherein said processor is embedded within an integrated circuit (figure 1) and wherein said reset information is generated in response to initialization of said integrated circuit (column 5, lines 18-20).

Allowable Subject Matter

4. Claims 19-24 are allowed. Prior art fails to disclose or suggest releasing said processor from said halt condition, said execution cycle remaining stopped and configuring at least one memory resource in communication with said processor after the processor is released from the halt condition and while the execution cycle of the processor is stopped.

Response to Arguments

5. Applicant's arguments regarding claims 13-18 filed 12/12/06 have been fully considered but they are not persuasive. Examiner notes that applicant's arguments of the rejection under 35 U.S.C. 102(e) are moot given the amendment and 35 U.S.C. 103 rejections above.
6. Applicant argues that Spiegel does not teach storing data at a memory location associated with a reset vector. Examiner agrees. Spiegel is used to teach at least one memory resource that is at a location

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associated with a reset vector. Sinclair discloses storing data at a location in memory. The combination would render obvious the claim limitation of configuring a memory resource by storing data at a location associated with a reset vector.

7. Applicant argues that the teachings of Sinclair would be destroyed by a combination with Spiegel. Examiner disagrees. As discussed above, the teachings of both references, taken in combination, would render obvious the invention to one of ordinary skill in the art at the time of invention. The exact workings of such a combination is not germane to the claim limitations if such details are not presented in the claim itself. Examiner also notes that the usage of the reset vector in the combination of the Sinclair and Spiegel teachings would not produce only one conceivable system. The reset vector could be used to point to a location within the SRAM.

8. Applicant's arguments regarding claims 19-24 are moot in view of the amendment to the claims and the subsequent allowability of these claims.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Anand Patel whose telephone number is (571) 272-7211. The examiner can normally be reached on Mon-Fri 8AM-4PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Rehana Perveen can be reached on (571) 272-3676. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

ABP



A. ELAMIN
PRIMARY EXAMINER